SYSTEM TIMING

/FS
24L
FS'
LINE PULSES
25L 25L

[SH1,IC10,PIN11] OA
[SH1,IC10,PIN09] CHB
[SH1,IC10,PIN10] OB
[SH1,IC10,PIN22] DAO
[SH1,IC10,PIN37] /FS
[SH1,IC39,PIN14] E
[SH1,IC39,PIN08] DAO'
[SH1,IC39,PIN09] /NMS
[SH1,IC10,PIN28] LUM
[SH1,IC10,PIN38] /HS
[SH1,IC39,PIN07] VCLK
[SH1,IC10,PIN33] CLK

IC29B 4053 B
IC29C 4053 C
IC29A 4053 A
IC28 2318
IC17 LM1889
IC13A LS32
IC14B LS74
IC14A LS74
IC31A LS123
IC30A LS74
IC18A LS86
IC23 74LS393
IC9A LS20
IC18C LS86
IC18B LS86
IC19D LS00
IC30B LS74
IC18D LS86
IC19C LS00
IC12 74LS393
IC9B LS20
IC19B LS00
IC13C LS32
IC13B LS32
IC13D LS32
IC19A LS00
IC24 LS390

C49 C
10K R55
10K R54
C48 C
C44 C
C30 C
C29 C
1K R51
4,43 XL2
1K R50
R53 2K7
3K3 R52
C31 C
C32 2-22p
15K R60
C33 C TNT
C38 C
R72 22K
C54 C
PIN#4 IS CONNECTED TO +5V IN THE ORIGINAL SCHEMATIC
C50 C
1K2 R58
R59 6K8
C45 C
24 LINE DELAY
C41 C
C51 C
C35 C
C34 C
C36 C
C37 C
R62 8K2
12K R63
R61 1K8
LINE PULSES

DISCONNECT FOR B/W
R41 1K2
C20 C TNT
R42 5K6
1K R45
C22 C
680R R44
C21 C
2N2369A TR2
R47 470R
220R R48
R46 270R
R43 10R
BC212L TR4
TR3 2N2369A
3K3 R57
R49 330R
R56 2K7
10K VR1
DC BALANCE
LK2
DIODE REFS ARE REVERSED IN THE ORIGINAL SCHEMATIC
D22 1N4148
D21 1N4148
(TO-92) LM7905 REG1
C24 C TNT
C25 C TNT

SOUND
+5V
COMPOSITE
VIDEO TO MONITOR
0V
-12V
+5V
+12V

RS 232 BOX

NOTE 1 * COMPONENTS FITTED FOR MKIII PSU ONLY

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